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SIMTEK CORP
Form 10-K/A
December 11, 2006

UNITED STATES
SECURITIES AND EXCHANGE COMMISSION
Washington, D.C. 20549

FORM 10-K/A
Amendment #2 to Form 10-K

- Annual report pursuant to section 13 or 15(d) of the Securities Exchange Act of 1934 for the fiscal year ended December 31, 2005
- Transition report pursuant to section 13 or 15(d) of the Securities Exchange Act of 1934.

Commission file number 0-19027

SIMTEK CORPORATION

(Exact name of registrant as specified in its charter)

Delaware
(State or other jurisdiction of incorporation or organization)

84-1057605
(I.R.S. Employer Identification No.)

4250 Buckingham Drive, Suite 100,
Colorado Springs, Colorado 80907
(Address of principal executive offices) (Zip Code)

(719) 531-9444
(Registrant's telephone number, including area code)

Securities registered pursuant to Section 12 (b) of the Exchange Act:
None

Securities registered pursuant to Section 12 (g) of the Exchange Act:
Common Stock \$.0001 Par Value OTC Bulletin Board
(Title of Class)

Indicate by check mark if the registrant is a well-known seasoned issuer, as defined in rule 405 of Securities Act.
Yes No
--- ---

Indicate by check mark if the registrant is not required to file reports pursuant to Section 13 or Section 15(d) of the Act. Yes No
--- ---

Indicate by check mark whether the issuer (1) filed all reports required to be filed by Section 13 or 15(d) of the Securities Exchange Act of 1934 during the preceding 12 months (or for such shorter period that the registrant was required to file such reports), and (2) has been subject to such filing requirements for the past 90 days. Yes No
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Indicate by check mark whether the registrant is a large accelerated filer, an

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accelerated filer or a non-accelerated filer. See definition of "accelerated filer and large accelerated filer" in Rule 12b-2 of the Exchange Act. (Check one):

Large Accelerated Filer	Accelerated Filer	Non-accelerated filer X
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Indicate by check mark whether the registrant is a shell company (as defined in Rule 12b-2 of the Exchange Act.)

Yes	No	X
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Aggregate market value of the voting and non-voting common equity held by non-affiliates of the registrant as of June 30, 2006, based upon the closing price of the common stock as reported by the OTC Electronic Bulletin Board on such date was approximately \$34,403,258. The total number of shares of Common Stock issued and outstanding as of December 5, 2006 was 16,147,746, after giving effect to the one for ten reverse stock split completed on October 5, 2006.

EXPLANATORY NOTE

This Form 10-K/A amends Part II Items 6 Selected Financial Data and 8 Financial Statements and Supplementary Data to reflect the reclassification of \$8,458,926 from Temporary Equity to Shareholders' Equity. Simtek Corporation originally concluded that the net proceeds from the sale of 68,750,000 shares of its common stock on December 30, 2005 should be recorded as Temporary Equity because of certain provisions in the Registration Rights Agreement entered into as part of the December 30, 2005 transaction described in Note 6 to the Consolidated Financial Statements. However, upon further review, management has determined that the transaction should be recorded in Shareholders' Equity. Except as set forth in this Explanatory Note, this Form 10-K/A does not amend or update any other information set forth in the Form 10-K/A originally filed by Simtek Corporation on April 28, 2006 or Form 10-K originally filed on April 7, 2006.

TABLE OF CONTENTS

PART I

Item 1:	Business.....	4
Item 1A:	Risk Factors.....	16
Item 1B:	Unresolved Staff Comments.....	25
Item 2:	Properties.....	25
Item 3:	Legal Proceedings.....	25
Item 4:	Submission of Matters to a Vote of Security Holders.....	25

PART II

Item 5:	Market for Registrant's Common Equity, Related Stockholder Matters and Issuer Purchase of Equity.....	27
	Securities.....	27
Item 6:	Selected Financial Data.....	29
Item 7:	Management's Discussion and Analysis of Financial Condition and Results of Operation.....	30
Item 7A:	Quantitative and Qualitative Disclosures About Market Risk....	46
Item 8:	Financial Statements and Supplementary Data.....	47
Item 9:	Changes in and Disagreements with Accountants on Accounting and Financial Disclosure.....	72
Item 9A:	Controls and Procedures.....	72
Item 9B:	Other Information.....	72

PART III

Item 10:	Directors and Executive Officers of the Registrant.....	73
Item 11:	Executive Compensation.....	76
Item 12:	Security Ownership of Certain Beneficial Owners and Management and Related Stockholder Matters.....	79
Item 13:	Certain Relationships and Related Transactions.....	82
Item 14:	Principal Accountant Fees and Services.....	84

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PART IV

Item 15: Exhibits and Financial Statement Schedules..... 86
Signatures..... 89

This annual report on Form 10-K/A contains statements which constitute forward-looking statements within the meaning of Section 21E of the Securities Exchange Act of 1934, as amended (the "Exchange Act"). Discussion containing such forward-looking statements may be found in the material set forth below and under "Business," as well as within the annual report generally. In addition, when used in this annual report, the words "believes," "anticipates," "expects," "plans," "intends" and similar expressions are intended to identify forward-looking statements. Forward-looking statements and statements of expectations, plans and intent are subject to a number of risks and uncertainties, including, but not limited to, those factors discussed under "Risk Factors" under Item 1A below. Actual results in the future could differ materially from those described in the forward-looking statements, as a result, among other things, of changes in technology, customer requirements and needs, our ability to access capital markets, wafer supplies and pricing, among other factors. We undertake no obligation to release publicly the results of any revisions to these forward-looking statements that may be made to reflect any future events or circumstances.

PART I

Item 1: Business

General

Simtek Corporation ("Simtek" or the "Company") provides integrated circuits to the electronics market for use in a variety of systems, such as data storage systems, computers, copiers, factory controllers, electric meters and military systems. We design, market and sell our products. We subcontract the majority of our manufacturing requirements. We have designed and developed nonvolatile static random access memory or "nvSRAM" products since we began business operations in May 1987 as a Colorado corporation. We have concentrated on the design and development of the 16, 64, 256-kilobit and 1-megabit nvSRAM product families and technologies, distribution channels, and sources of supply,

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including production at subcontractors. Kilobits are a measure of the amount of data that can be stored; more kilobits imply more storage. Megabits are also a measure of the amount of data that can be stored; there are 1,000 kilobits in one megabit. During 2000, we added the capability to design, develop and produce programmed semiconductor logic products. However, during 2003, due to adverse market conditions, we decided to no longer offer our programmed semiconductor logic products after December 31, 2003.

On December 30, 2005, we acquired from Zentrum Mikroelektronik Dresden AG ("ZMD") certain assets related to ZMD's nvSRAM product line (the "ZMD Asset Acquisition"). On that same date and in connection with the ZMD Asset Acquisition, which is described in more detail below, we entered into a number of agreements including a License Agreement (the "New License Agreement") with ZMD. Pursuant to the New License Agreement, ZMD assigned its rights in certain patents devoted to nvSRAM to us and we licensed to ZMD the right to use our silicon-oxide-nitride-oxide-silicon (SONOS)-based nvSRAM technology for embedded functions in ZMD's non-competing mixed signal and analog Application Specific Integrated Circuit (ASIC), System on Chip (SoC) and Application Specific Standard Product (ASSP) products. The licenses granted pursuant to the New License Agreement are perpetual, non-exclusive, royalty-free and unlimited. No fees or payments are due to either party under the New License Agreement. The New License Agreement shall remain in effect on a country-by-country basis until

4

all patents, trade secrets, and any other proprietary and legal rights subject thereto have expired or ended, unless terminated earlier by either party following a breach by the other party that remains uncured after 30 days' written notice.

On the same date, we executed a Non-Competition and Non-Solicitation Agreement with ZMD whereby, for a period of five years from the closing, ZMD is prohibited from competing with certain of our products and from hiring our employees in certain situations. The parties also executed a Registration Rights Agreement whereby we agreed to register under the Securities Act of 1933, as amended (the "Securities Act"), for resale, subject to certain limitations, the shares issued to ZMD pursuant to the ZMD Asset Acquisition. We have been party to various product license arrangements and cooperation agreements for over the last 11 years with ZMD. The new agreements replace all previous agreements.

In January 2006, we formed Simtek GMBH in Dresden Germany, as our wholly-owned subsidiary. As of March 31, 2006, Simtek GMBH had 9 employees, including 4 engineers, a sales manager, a customer service person, and 3 administrative employees. This new subsidiary will be our new sales and marketing center for European customers. The engineers in Dresden will work as an integral part of our company-wide design and support engineering team.

In May of 2005, we entered into a Production and Development Agreement with Cypress Semiconductor Corporation ("Cypress") to cooperate in developing a semiconductor process module that combines our nonvolatile technology with Cypress' advanced 0.13-micron complementary metal-oxide semiconductor, or CMOS, fabrication line. The module incorporates SONOS technology, which is used to manufacture both high-density SONOS flash and SONOS nvSRAM products, for stand alone and embedded products. During 2005, our research and development team along with Cypress' research and development team worked aggressively on the co-development program.

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In 2005 we determined that Q-DOT, our wholly owned subsidiary that specialized in advanced technology research and development for data acquisition, signal processing, imaging and data communications, no longer fit with our core non-volatile memory business. We had acquired Q-DOT in March 2001 in an effort to enter the high speed data communications market, addressing both wired and wireless applications, based on advanced "silicon germanium" process technology. On August 30, 2005, we along with Q-DOT, entered into an Asset Purchase Agreement with Hittite Microwave Corporation ("Hittite") and a wholly-owned subsidiary of Hittite, HMC Acquisition Corporation ("HMC Acquisition"), whereby substantially all of the assets of Q-DOT were sold to HMC Acquisition in exchange for a cash payment of approximately \$2.2 million. The Company realized a net gain of approximately \$1,687,000. In addition, Hittite assumed certain future obligations of Q-DOT, including obligations related to Q-DOT's real estate lease and certain software license agreements. Incident to the Asset Purchase Agreement, the parties also entered an Escrow Agreement, whereby \$200,000 of the purchase price was placed in escrow for one year to secure certain indemnification obligations of Simtek and Q-DOT. In addition, the parties entered into a Confidentiality, Non-Disclosure and Restrictive Covenant Agreement, whereby, among other things, Simtek and Q-DOT agreed not to compete against Hittite and HMC Acquisition for a period of four years with respect to certain businesses relating to Q-DOT's operations.

5

As of December 31, 2005, our backlog for released purchase orders was approximately \$2,559,000, all of which is expected to ship by June 30, 2006. Comparatively, our backlog for released purchase orders was approximately \$1,971,000 as of December 31, 2004. Orders are generally cancelable without penalty at the option of the purchaser prior to 30 days before scheduled shipment and, therefore, are not necessarily a measure of future product revenue.

We are in production of our family of memory products. During 2004, we transferred the production of our 0.8-micron family of nvSRAM products from Chartered Semiconductor Manufacturing Plc. of Singapore, or ("Chartered"), facility #1 to Chartered's facility #2. We qualified our 0.8-micron family of nvSRAM products built from wafers received from Chartered's facility #2 for sales into commercial, industrial and military markets during late 2004 and early 2005. We refer to these products as our legacy products. During 2003, we designed and began sampling our 1-megabit nvSRAM product for sale into commercial and industrial markets. In September 2005, we qualified our 1-megabit nvSRAM products built on 0.25-micron silicon wafers we receive from DongbuAnam Semiconductor or "DongbuAnam". We have expanded our product family of nvSRAM products built on silicon wafers received from DongbuAnam to include a 1-megabit nvSRAM with real time clock, a 256-kilobit nvSRAM and a 256-kilobit nvSRAM with real time clock. We anticipate qualification of these three products to occur in the second quarter of 2006. We refer to the product family built on silicon wafers we receive from DonguAnam as 0.25-micron products. Our nvSRAM products are physically smaller and require less maintenance than static random access memory devices that achieve nonvolatility through the use of internal batteries and are more convenient to use than static random access memory devices that achieve nonvolatility by being combined with additional chips.

We reduce our capital requirements by subcontracting the majority of the manufacturing process to third parties. Chartered began providing silicon wafers, produced from its facility #1, for our nvSRAM memory products in September 1993 and continues to provide wafers based on our product technology from its facility #2. We began receiving our memory wafers manufactured in

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Chartered's Facility #2 in late second quarter of 2004 and through the third quarter of 2004. However, with this process being transferred to an alternative manufacturing facility, we saw lower than average production yields during 2004, which in turn lowered our gross margins. During 2005, our average production yields returned to or above historic levels.

We entered into a Process Transfer Agreement with X-FAB Texas, Inc., of Texas, or X-FAB, to install our silicon-nitride-oxide-semiconductor technology into its wafer fabrication facility to provide an additional manufacturing source to material supplied by Chartered. Due to a lack of Simtek and X-FAB resources required to install our nonvolatile semiconductor memory process into X-FAB and the marginal anticipated return-on-investment, we canceled the project with X-FAB in August 2004.

6

United Microelectronics and Chartered provided silicon wafers for our programmed semiconductor logic products based on 0.5-micron and 0.35-micron process technology, respectively. In February 2003, we received notification from United Microelectronics that it was unable to supply us with logic wafers after August 2003. We supported customers with 0.5-micron logic wafers manufactured at United Microelectronics through December 2003 by offering opportunities to purchase their lifetime requirements for these products with deliveries at the end of 2003. These products have been discontinued and are no longer offered to customers.

Amkor Technology, located in the Philippines, provides assembly services and Integra Technologies Advanced Semiconductor Engineering Inc., located in Kansas, provides final test services, for our nvSRAM products. In 2006, we plan to move the final testing of our high volume products to more cost effective local sub-contract manufacturing partners located in Asia.

During 2005, all of the wafers used to produce our 0.8-micron nvSRAM's were purchased from Chartered. Sales of these products accounted for approximately 86% of our revenue for 2005. Wafers were purchased from DongbuAnam in 2005 to support our 0.25-micron products. Sales of these products accounted for approximately 13% of our revenue for 2005. The remaining sales for the year were from miscellaneous products.

Memory Industry and Product Background

The semiconductor memory market is large and highly differentiated. This market covers a wide range of product densities, speeds, features and prices. We believe that the ideal memory product would have:

- o high bit density per chip to minimize the number of chips required in a system;
- o fast data read and write speeds to allow a system's microprocessor to access data without having to wait;
- o the ability to read and modify data an unlimited number of times;

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- o low power consumption;
- o the ability to retain its data indefinitely when power is interrupted (i.e. nonvolatility);
- o availability in a variety of package types for modern assembly techniques; and
- o the ability to be tested completely by the manufacturer to ensure the highest quality and reliability.

The memory market is segmented with different products combining different mixes of these attributes.

Semiconductor memories can be divided into two main categories, volatile and nonvolatile. Volatile memories generally offer high densities and fast data access and programming speeds, but lose data when electrical power is interrupted. Nonvolatile memories retain data in the absence of electrical power, but typically have been subject to speed and testing limitations. They also wear out if they are modified too many times. There are a number of common volatile and nonvolatile product types, as set forth below. The list of products under "Combinations" is limited to single packages and does not include

7

combinations of the listed memories in separate packages, such as static random access memories in combination with Electrically Erasable Programmable Read Only Memories and Erasable Programmable Read Only Memories.

Volatile -----	Nonvolatile -----	Combinations -----
Static Random Access Memories (SRAM)	Electrically Erasable Programmable Read Only Memory (EEPROM)	Nonvolatile Static Random Access Memory (nvSRAM)
Dynamic Random Access Memory (DRAM)	Flash Memory (FLASH)	Nonvolatile Random Access Memory (nvRAM)
	Erasable Programmable Read Only Memory (EEPROM)	Static Random Access Memory with lithium battery (BatRAM)
	Programmable Read Only Memory	
	Read Only Memory (PROM or ROM)	

Volatile Memories. Rewritable semiconductor memories store varying amounts of electronic charge within individual memory cells to perform the memory function. In a Dynamic Random Access Memory the charge must be electrically refreshed many times per second or data are lost even when power is continuously applied. In a static random access memory the charge need not be refreshed, but data can be retained only if power is not interrupted.

Nonvolatile Memories. A Read Only Memory is programmed, or written, once in the later stages of the manufacturing process and cannot be reprogrammed by the user. Programmable Read Only Memory can be programmed once by the user, while Erasable Programmable Read Only Memory may be reprogrammed by the user a limited number of times if the Erasable Programmable Read Only Memory is removed from the circuit board in the equipment. Both Flash Memory and Electrically Erasable Programmable Read Only Memory may be reprogrammed electrically by the user

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without removing the memory from the equipment. However, the reprogramming time on both Electrically Erasable Programmable Read Only Memory and Flash Memory is excessively long compared to the read time such that in most systems the microprocessor must stop for a relatively long time to rewrite the memory.

Combinations. Many customers use a combination of volatile and nonvolatile memory functions to achieve the desired performance for their electronic systems. By using static random access memories in combination with Erasable Programmable Read Only Memory and Electrically Erasable Programmable Read Only Memory chips, customers can achieve nonvolatility in their systems and still retain the high data read and write speeds associated with static random access memory. This approach, however, is not desirable in many applications because of the size and cost disadvantages associated with using two or more chips to provide a single memory function. Also, it may take up to several seconds to transfer the data from the static random access memory to the Electrically Erasable Programmable Read Only Memory; an excessive time at power loss. As a result, attempts have been made to combine nonvolatile and volatile memory features in a single package or silicon chip. One approach combines a static random access memory with lithium batteries in a single package, which is called battery-backed SRAM.

8

Nonvolatile random access memories combine volatile and nonvolatile memory cells on a single chip and do not require a battery. We believe our nvSRAM products represent a significant advance over existing products that combine volatility and nonvolatility on a single silicon chip. We combine a static random access memory cell with an Electrically Erasable Programmable Read Only Memory cell to create a small nvSRAM cell. Our unique and patented memory cell design enables the nvSRAM product to be produced at densities higher than existing nonvolatile random access memories and at a lower cost per bit. In addition to high density and nonvolatility, the nvSRAM product has fast data access and program speeds and the static random access memory portion of the memory can be modified an unlimited number of times without wearing out.

We use an advanced implementation of SONOS technology. SONOS technology stores electrical charge within an insulator, silicon nitride, and uses a thin tunnel oxide layer to separate the silicon nitride layer from the underlying silicon substrate. Silicon-nitride-oxide-semiconductor technology prevents tunnel oxide rupture in the memory cell from causing an immediate loss of data. Oxide rupture has been a major cause of failures in Flash and Electrically Erasable Programmable Read Only Memories using floating gate technology, where charge is stored on a polysilicon conductor surrounded by insulators. To protect against these failures, many floating gate Electrically Erasable Programmable Read Only Memories have required error correction circuitry and redundant memory cells. This increases product cost by requiring more silicon area. Error correction and redundancy are not required for our products to protect against tunnel oxide rupture. In addition, our product designs incorporate a special test feature that can predict data retention time for every individual memory cell based on measuring the rate of charge loss out of the silicon nitride. Our latest 0.25-micron technology adds an additional oxide layer, forming a silicon-oxide-nitride-oxide-semiconductor stack, to support finer geometry electrical performance.

The silicon-nitride-oxide-semiconductor technology coupled with our patented nvSRAM cell allows high performance nvSRAM's to be manufactured using complementary metal oxide semiconductor technology. The

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Silicon-nitride-oxide-semiconductor technology that we use has proven to be highly reliable, as demonstrated by our product qualification results to date.

Our Memory Products

Nonvolatile Static Random Access Memories ("nvSRAM"). Our nvSRAM product family consists of nonvolatile memories that combine fast static random access memory and nonvolatile elements within each memory cell on a single chip of silicon. The static random access memory portion of the nvSRAM product is operated in the same manner as most standard static random access memory products. The static random access memory can be written to and read from an unlimited number of times. The nonvolatile elements can be programmed, depending upon device type, by user control or automatically by transferring the static random access memory contents into the nonvolatile element memory. The data stored in the nonvolatile elements can be transferred back into the static random access memory by user control or the data can be transferred automatically.

Our nvSRAM products have fast data access speeds of 25, 35 and 45 nanoseconds. These data access speeds correspond to those of fast static random access memory and, we believe, meet the requirements of much of the fast static random access memory market. The high-speed characteristics of our nvSRAM products allow them to be used in applications with various high performance microprocessors and digital signal processors such as those manufactured by Intel Corp., Texas Instruments and Freescale. Our nvSRAM products can be used to replace static random access memories with lithium batteries and multiple chip solutions such as static random access memory plus Electrically Erasable Programmable Read Only Memory or Flash Memory.

9

The various combinations of density and speed allow our nvSRAM products to meet the design and performance requirements of many different types of systems.

Our newest nvSRAM architecture, currently implemented in our 0.25-micron product family, adds an eight-bit micro-controller, approximately 20,000 gates of metal-programmable logic and programmable input-output devices. We refer to this architecture as Value-Added-Memory . It is designed to allow variations of the base-line 1-megabit nvSRAM design to be quickly developed for emerging market applications.

We finalized commercial and industrial qualification of two versions of our initial 64-kilobit nvSRAM product offering in September 1991 and April 1992, respectively. We completed military qualification of our initial nvSRAM's in May 1992. We began sales into the commercial market of our initial 16-kilobit nvSRAM product family in 1992. We completed the development and product qualification of the 64-kilobit AutoStore™ nvSRAM in 1993. The AutoStore™ version automatically detects power loss and transfers the data from the static random access memory cells into the Electrically Erasable Programmable Read Only Memory cells. This device does not require instructions or intervention from the system microprocessor to notify it of the power loss. Commercial and industrial qualification of our 256-kilobit nvSRAM occurred in 1997 and military qualification of our 256-kilobit nvSRAM was completed in the second quarter of 1998. In 2002, we qualified our 3-volt 256-kilobit nvSRAM for use in commercial and industrial applications. During 2003, we designed and began sampling our

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1-megabit nvSRAM product for sale into commercial and industrial markets. Qualification of our 1-megabit nvSRAM product occurred in September 2005. We qualified our 0.8-micron family of nvSRAM products built from wafers received from Chartered's facility #2 for sales into commercial, industrial and military markets during late 2004 and early 2005.

Product Warranties

We presently provide a one-year limited warranty on our products.

Research and Development

Our research and development activities are centered on developing new nvSRAM products. We also continually work to improve yields and reduce costs on all of our qualified products. In order to reduce costs, since late 1997 we have used outside experts for testing our products. In addition, we have a test floor which is used for evaluation of our technologies, product designs and product quality.

10

In October 2001, we entered into an agreement with what is now DongbuAnam Semiconductors to develop a semiconductor process module that combines our nonvolatile technology with DongbuAnam's advanced 0.25-micron complementary metal-oxide semiconductor, or CMOS, fabrication line. CMOS is the semiconductor technology used in the transistors that are manufactured for most of today's computer microchips. The module incorporates SONOS technology, which is used to manufacture both high-density SONOS flash and SONOS nvSRAM's, for stand alone and embedded products. During 2002 and 2003, our research and development team along with DongbuAnam's research and development team worked on the co-development program. Our 1-megabit 3-volt nvSRAM was the primary development vehicle. In August 2003, we received the first complete processed silicon from this development, which yielded working samples of our new 1-megabit 3-volt nonvolatile semiconductor memory product. We began shipping samples of our new 1-megabit 3-volt nonvolatile semiconductor memory product in September 2003. In September 2005, we completed the full qualification of our 1-megabit 3-volt nonvolatile semiconductor memory product for use in the commercial and industrial market.

In May of 2005, we entered into a Production and Development Agreement with Cypress to cooperate in developing a semiconductor process module that combines our nonvolatile technology with Cypress' advanced 0.13-micron digital complementary metal-oxide semiconductor, or CMOS, fabrication line. The module incorporates SONOS technology, which is used to manufacture both high-density SONOS flash and SONOS nvSRAM's, for stand alone and embedded products. During 2005, our research and development team along with Cypress' research and development team worked on the co-development program.

Our research and development expenditures for the years ended December 31, 2005, 2004 and 2003 were \$6,369,109, \$4,942,391 and \$3,987,054, respectively. We expect to continue expenditures on research and development, as we expand our products to include 4 megabit and greater memory densities.

Manufacturing and Quality Control

Our manufacturing strategy is to use subcontractors whose production

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capabilities meet the requirements of our product designs and technologies.

Since 1993, Chartered has provided us with silicon wafers for our legacy products.

DongbuAnam provides silicon wafers for our 0.25-micron process to support our 1-megabit product family.

Device packaging of our nvSRAM products continues at the Amkor facilities in the Philippines and Advanced Semiconductor Eng. Inc., in Taiwan. Final test for our nvSRAM products continues with Integra Technologies, formerly Amkor Test Services, in Wichita, Kansas, although as previously stated, we plan to move this function to more cost effective locations in Asia in 2006.

11

Our subcontractors provide quality control for the manufacture of our products. We maintain our own quality assurance personnel and testing capability to assist the subcontractors with their quality programs and to perform periodic audits of the subcontractors' facilities and finished products to ensure product integrity.

We maintained our certification to the ISO9001:2000 Quality Management System for our internal operations in Colorado Springs through 2005 through successful maintenance audits. Our major subcontractors also support ISO-14001 Environmental Control certifications. We continue to support our Mil-Prf-38535 Appendix A quality system in support of our SMD and military grade products.

Our quality and reliability programs were audited by several major commercial customers and also by Defense Supply Center Columbus or "DSCC" during 2005 as part of routine supplier certification procedures. All such audits were completed satisfactorily. We have established a RoHS compliance program and are able to offer fully compliant versions of our products meeting our customers' deadline of July 1, 2006. We plan to continue to offer both compliant and non-compliant plating types until demand for the tin/lead plating falls significantly. RoHS stands for Reduction of Hazardous Substances, which principally requires the elimination of lead used in products.

Markets

Our memory products are targeted at fast nvSRAM markets, static random access memory plus Electrically Erasable Programmable Read Only Memory markets and other nonvolatile memory products broadly used in commercial, industrial and military electronic systems.

Our products are typically used to store critical data when power is removed from the system. Often this data must be captured very quickly and we believe that the fast write time of our nvSRAM products is a significant benefit over nonvolatile memory alternatives. Our products are used in systems that are "write intensive" such as data collection, event recording and others where we believe that the unlimited write endurance of our nvSRAM is superior to alternative nonvolatile memory solutions.

We expect to see increasing revenue due to three major factors. The revenue contribution of 1-megabit nvSRAM products is increasing as Simtek enters 2006. Overall corporate average selling prices are expected to increase, as customers

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migrate from lower density products to higher priced, higher density 1-megabit nvSRAMs. Finally, we are seeing increased volume of our legacy products, as targeted end applications continue to experience additional nvSRAM product adoption and end market growth.

TARGET APPLICATIONS FOR SIMTEK PRODUCTS

Airborne Computers
Automotive Control & Monitoring Systems
Automated Teller Machines

Lighting Control Systems
Medical Instruments
Currency Changers

12

Data Monitoring and Recording Equipment
Process Control Equipment
Down Hole Drilling Systems
Gaming Machines
GPS Navigational Systems
Guidance and Targeting Systems
High Performance Workstations
Laser Printers
Weapon Control Systems
Copiers
Cable TV and Satellite Set Top Converter Boxes
Multi- Function Printers
RAID Controllers
Robotics
LCD Projectors
Power Grid Management Systems
Postal Systems
Automated Parking Systems

Printers
Facsimile Machines
Radar and Sonar Systems
Telecommunications Systems
POS Terminals
Automated Test Equipment
Utility Meters
Routers
Security Systems
Broadcast Equipment
Studio Recording Equipment
Servers
Factory Automation Systems
Mass Storage Systems
Irrigation Systems Controllers
Fluid Flow Meters
Motor Controllers
Train Control Systems

Our 1-megabit nvSRAM is opening new applications into which our products are being designed. These include designs into an entirely new generation of integrated Redundant Array of Independent Disks or "RAID" controllers, power metering, airborne communications, and data communications.

We believe that for 2006, our marketing and sales focus will be to better penetrate new markets for Simtek such as external RAID arrays and mass storage subsystems, while further penetrating emerging data communications and new automotive applications.

Sales and Distribution

Our strategy is to generate sales through the use of independent sales representative agencies and distributors supported by Simtek sales and technical personnel. We believe this strategy provides the fastest and most cost effective way to assemble a large and professional sales force.

We currently have four sales and marketing offices. They are located in Colorado, Georgia and California for the North American and Asian markets, and in Dresden Germany for the European market. We have engaged approximately 20 sales independent representative organizations and approximately 30 distributor organizations with sales offices worldwide. These organizations have multiple sales offices and technical sales personnel covering specific geographic territories. Through these organizations and their sales offices we believe that we are capable of serving a significant portion of the worldwide market for our

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products with our full line of products.

Independent sales representatives typically sell a limited number of non-competing products to semiconductor users in particular assigned geographic territories. Distributors inventory and sell products from a larger number of

13

product lines to a broader customer base. These sales channels are generally complementary, as representatives and distributors often work together to consummate a sale, with the representative receiving a commission from us and the distributor earning a markup on the sale of products. We supply sales support and materials to the sales representatives and distributors.

For our marketing activities, we evaluate external marketing surveys and forecasts and perform internal studies based, in part, on inputs from our independent sales representative agencies. Marketing decisions are also based on forecasts and inputs from our current and prospective customers. We prepare brochures, data sheets, application notes, product collateral and product advertising with our internal marketing resources and contracted outside services. Much of this information can be found on the Simtek website at www.simtek.com.

Customers and Backlog

We have shipped qualified nvSRAM products to customers directly and through distributors since our initial commercial product qualification in September 1991. The majority of our sales are to Fortune 500 companies. Sales by geographic area for the years ended December 31, 2005, 2004, and 2003 were as follows (as a percentage of sales):

	2005	2004	2003
	----	----	----
United States	26%	29%	37%
Europe	18%	11%	12%
Far East:			
China	5%	0%	0%
Japan	11%	12%	20%
Singapore	15%	11%	20%
Taiwan	11%	17%	3%
Thailand	2%	6%	0%
Far East Other	2%	2%	3%
Other	10%	12%	5%
Total	100%	100%	100%

As of December 31, 2005, we had a backlog of unshipped customer orders of approximately \$2,559,000, which we expect to ship by June 30, 2006. Comparatively, our backlog for unshipped customer orders was approximately \$1,971,000 as of December 31, 2004. Orders are cancelable without penalty at the option of the purchaser prior to 30 days before scheduled shipment and therefore are not necessarily a measure of future product revenue.

Competition

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Our products compete on the basis of several factors, including data access speeds, programming speeds, density, data retention, reliability, programming endurance, space savings, manufacturability, ease of use and price.

14

Simtek's products fall into a memory category commonly referred to as nvRAM (nonvolatile Random Access Memory). nvRAM products that compete with our family of nvSRAM products fall into two categories.

The first category of products that compete with our nvSRAM products is products that combine static random access memories, power management devices with lithium batteries in specially adapted packages. These products generally are slower in access speeds than our nvSRAM products due in part to limitations caused by the life of the lithium battery when coupled with a faster static random access memory. Our nvSRAM products are offered in standard, smaller, less expensive packages, and do not have the limitation on lifetime imposed on the static random access memory/battery solutions by the lithium battery. Our nvSRAM products eliminate common problems associated with batteries such as corrosion, premature wear-out, shelf-life maintenance, inventory management and leaded content. Our nvSRAM's can also be used for wave soldered automatic insertion circuit board assembly since they do not have the temperature limitations of lithium batteries. However, lithium battery-backed static random access memory products are available in densities of 16-megabit and greater per package. Companies currently supplying products with lithium batteries include Maxim, ST Microelectronics and Texas Instruments.

The second category consists of ferroelectric random access memory or commonly referred to as FRAM. FRAM memories use a capacitor with a ferroelectric dielectric as a storage element and a specialized transistor as a selection element. The use of ferroelectric materials for nvRAM has been researched for more than three decades but only few companies have been able to commercialized the technology. The major reason appears to be the very challenging manufacturing process. Typically capacities of FRAM are small. FRAM is considered a solution in applications that require low densities and low power where it has a competitive advantage over our nvSRAMs. FRAM's major disadvantage is limited memory endurance due to the destructive nature of the read out cycle. The major sources for FRAM stand alone memory components are Ramtron, OKI and Fujitsu. While other companies such as TI have licensed the FRAM technology from Ramtron, it is expected that TI will embed the FRAM memory into a more complex ASSP.

Based on market research data from Web Feet Research, we estimate the Simtek nvSRAM market share to be approximately 7% of the total nvRAM market. However, with increasing revenues, this market share is expected to increase in 2006.

We are aware of other semiconductor technologies for nonvolatile memory products. Each of these requires a newly developed process technology, which has processing risk, but may deliver performance characteristics superior to our technology if perfected. Each of these processes integrates materials into the silicon processing steps that are not commonly used for semiconductor memory products today. If successful, these products could perform the same functions in a system that our products currently perform, but may be manufactured in higher density or lower cost products. Freescale is believed to be developing such magnetic film products.

Patents and Intellectual Property

We undertake to protect our product designs and technologies under the relevant intellectual property laws as well as by utilizing internal disclosure safeguards. Under our licensing programs, we exercise control over the use of our protected intellectual property and have not permitted our licensees to sublicense our nvSRAM products or technology.

It is common in the semiconductor industry for companies to obtain copyright, trademark, trade secret and patent protection of their intellectual property. We believe that patents are significant in our industry, and we are seeking to build a patent portfolio. We expect to enter into patent license and cross-license agreements with other companies. We have been issued 17 patents in the United States on our nvSRAM cell and other circuit designs. These patents relate to circuit implementations used to design our products. The use of these patents allows us to design circuits with lower power consumption and faster store timing than would be possible otherwise giving us a competitive advantage over other technologies. These patents have terms that expire from 2006 to 2018. We have also taken steps to obtain European patents in the large European countries, including Germany, France, the United Kingdom and Sweden on the nonvolatile memory patents that would have potential value in international markets. We have four applications that have been allowed and intend to prepare patent applications on additional circuit designs we have developed. However, as with many companies in the semiconductor industry, it may become necessary or desirable in the future for us to obtain licenses from others relating to our products.

We also protect aspects of our technology that relate to our semiconductor memory products as trade secrets. There are disadvantages to protecting intellectual property as trade secrets rather than patents. Unlike patents, trade secrets must remain confidential in order to retain protection as proprietary intellectual property. We cannot assure you that our trade secrets will remain confidential. If we lose trade secret protection, our business could suffer.

We have received federal registration of the term "Novcel" a term we use to describe our technology. We have not sought federal registration of any other trademarks, including "Simtek" and "QuantumTrap™" or our logo.

Late in 2002 and in 2003, we were contacted by Syndia Corporation regarding possible infringement on certain patents. Syndia Corporation informed us that it had acquired a portfolio patents issued to Jerome Lemelson. This patent portfolio was not included in the portfolio owned by Lemelson Foundation Partnership, an entity with which we reached a licensing agreement in 1999. We are currently reviewing any potential infringements. If there are any infringements, we believe that we can reach a reasonable licensing agreement with Syndia Corporation.

Employees

As of the date of the Form 10-K filed on April 7, 2006, we had 52 full-time employees.

Item 1A. Risk Factors

LIMITED OPERATING CAPITAL AND ABILITY TO RAISE ADDITIONAL MONEY MAY HARM OUR ABILITY TO DEVELOP AND MARKET PRODUCTS

We require significant capital for product development, subcontracted production, and marketing. We have funded these needs from the sale of products, the sale of product and technology licenses, from royalties, as well as from the sale of our convertible debt and equity securities.

We have not seen any significant increase in product sales in the past year and gross margins are less than anticipated. Our cash requirements have been difficult to meet. We cannot guarantee that we will be able to achieve an increase in product sales and gross margins. We may need more capital in the future to develop new products. We are not sure that we will be able to raise more capital on reasonable terms, if at all. If we cannot, then we may not be able to develop and market new products. The development, subcontracted production and marketing of existing products may also suffer, causing our financial position and stock price to deteriorate.

WE MAY EXPERIENCE OPERATING LOSSES IN THE NEXT SEVERAL YEARS

We began business in 1987. Through December 31, 2005, we had accumulated losses of approximately \$46.0 million. We realized net income for the first time for the year ended December 31, 1997 and continued to realize net income through June 30, 2000. Subsequent to June 30, 2000 and through December 31, 2005, we realized net losses primarily as a result of accounting charges, from the purchase of incomplete research and development in September 2000, decreased revenue, decreased gross margins, increased competitive pressures and increased research and development costs related to new product development. We may continue to experience net operating losses in the future. Continuing net operating losses could materially harm our results of operations, increase our need for additional capital in the future, and hurt our stock price.

WE MIGHT NOT BE ABLE TO RE-GAIN COMPLIANCE WITH CERTAIN COVENANTS SET FORTH IN OUR LOAN AGREEMENT WITH THE RENN CAPITAL GROUP; IF WE ARE UNABLE TO DO SO, THE RENN CAPITAL GROUP COULD ACCELERATE THE \$3 MILLION LOAN AND FORECLOSE ON THE COLLATERAL THAT WE GRANTED TO IT

Our loan agreement with Renaissance Capital Growth and Income Fund III, Inc., Renaissance US Growth Investment Trust PLC and BFSUS Special Opportunities Trust PLC, or the RENN Capital Group, formerly Renaissance Capital Group, Inc., contains various financial covenants. As of December 31, 2005, we were not in compliance with two of the covenants set forth in the loan agreement, which covenants relate to the interest coverage ratio and debt to equity ratio. On March 21, 2006, we received a waiver for the two covenants through January 1, 2007. However, significant variances in future actual operations from our current estimates could result in the reclassification of this note to a current liability. If the note becomes due and we cannot pay it, RENN Capital Group may foreclose on the assets that we pledged as security for the note. This would significantly harm our business.

WE MAY BE UNABLE TO SUCCESSFULLY INTEGRATE THE ASSETS ACQUIRED FROM ZMD ON DECEMBER 30, 2005

On December 30, 2005, we closed our acquisition from ZMD of certain intellectual property and assets related to ZMD's nvSRAM product line. We may be unable to integrate successfully into our operations the assets acquired from ZMD, including:

- o by a failure to gain customer agreement to purchase products from us or to qualify our designs or processes;
- o by a failure to coordinate international operations, relationships and facilities, which may be subject to additional constraints imposed by geographic distance, local laws and regulations; and
- o by a failure to implement and maintain uniform standards, internal controls, business processes, procedures, policies and information systems.

Our failure to meet any of these challenges could cause us to fail to realize any accretive benefits of the acquisition of the assets from ZMD and could seriously harm our results of operations.

WE MAY BE UNABLE TO RETAIN AN EFFECTIVE FOCUS IN OUR INDUSTRY OR RETAIN CUSTOMERS FOLLOWING THE ACQUISITION OF ASSETS FROM ZMD

The challenges to us as a result of the acquisition of certain intellectual property and assets from ZMD on December 30, 2005 include:

- o communicating a strategic vision to the market regarding us and executing on that strategic vision;
- o implementing sales and marketing efforts to effectively communicate to customers our capabilities;
- o overcoming possible concerns of certain customers about not having two sources of supply for the products they previously purchased from both ZMD and us;

- o gaining acceptance from former ZMD customers for our designs, products or processes; and
- o overcoming any perceived adverse changes in business focus, including demonstrating to customers that the acquisition of certain assets from ZMD will not result in an adverse change in customer service standards or business focus and helping customers conduct business easily with us going forward.

The failure to meet any of these challenges could seriously hinder our plans for product development as well as business and market expansion following the acquisition of certain intellectual property and assets from ZMD.

IF WE CANNOT RECEIVE SILICON WAFERS WE REQUIRE TO MANUFACTURE OUR PRODUCTS FROM OUR SILICON WAFER MANUFACTURERS AT THE VOLUMES OR THE PRICES WE REQUIRE, OUR REVENUES, EARNINGS AND STOCK PRICE COULD SUFFER

We currently purchase the silicon wafers we require to build our non-volatile memory products from three vendors, Chartered Semiconductor Manufacturing Plc. of Singapore, DongbuAnam Semiconductors in Korea and ZFoundry in Germany. Due to the volatility of the semiconductor market, we have limited control over the pricing and availability of the wafers we require in order to build our products. The risk of not receiving the products and pricing we need to achieve our revenue objectives has escalated. If we are unable to obtain the products and pricing we need from these vendors, our business could suffer.

THE UNCERTAINTY INVOLVED IN MANUFACTURING SEMICONDUCTORS MAY INCREASE THE COSTS AND DECREASE THE PRODUCTION OF OUR PRODUCTS

In order for us to become profitable, we must drive our manufacturing costs down and secure the production of sufficient product. Semiconductor manufacturing depends on many factors that are complex and beyond our control and often beyond the control of our subcontractors. These factors include contaminants in the manufacturing environment, impurities in the raw materials used and equipment malfunctions. Under our arrangements with our subcontractors, our subcontractors pass on to us substantially all of their costs that are unique to the manufacture of our products. Accordingly, these factors could increase the cost of manufacturing our products and decrease our profits. These factors could also reduce the number of semiconductor memories that our subcontractors are able to make in a production run. If our subcontractors produce fewer of our products, our revenues may decline.

DELAYS IN MANUFACTURING MAY NEGATIVELY IMPACT OUR REVENUE AND NET INCOME

It takes approximately three months for our subcontractors to manufacture our semiconductor memories. Any delays in receiving silicon wafers or completed products from our subcontractors will delay our ability to deliver our products to customers. This would delay sales revenue and could cause our customers to cancel existing orders or not place future orders. These delays could occur at any time and would affect our net income.

WE DEPEND ON INDEPENDENT SALES REPRESENTATIVES AND DISTRIBUTORS TO SELL OUR PRODUCTS AND THE TERMINATION OF ANY OF THESE RELATIONSHIPS MAY HARM OUR REVENUE

We use independent sales representatives and distributors to sell the majority of our products. The agreements with these sales representatives and distributors can be terminated without cause by either party with 30 to 90 days written notice. If one or more of our sales representatives or distributors terminates our relationship, we may not be able to find replacement sales representatives and distributors on acceptable terms or at all. This could affect our profitability. In addition, during 2005, 2004 and 2003 approximately 51%, 50% and 42% of our product sales were to four distributors. We cannot be certain that we will be able to maintain our relationship with these distributors.

DELAYS IN OR FAILURE OF PRODUCT QUALIFICATION MAY HARM OUR BUSINESS

Prior to selling a product, we must establish that it meets expected performance and reliability standards. As part of this testing process, known as product qualification, we subject representative samples of products to a variety of tests to ensure that performance in accordance with commercial, industrial and military specifications, as applicable. If we are unable to successfully accomplish product qualification for our future products, we will be unable to sell these future products. Even with successful initial product qualifications, we cannot be assured that we will be able to maintain product qualification or achieve sufficient sales to meet our operating requirements.

OUR SUCCESS DEPENDS ON OUR ABILITY TO INTRODUCE NEW PRODUCTS

The semiconductor industry is characterized by rapid changes in technology and product obsolescence. Our success in the semiconductor industry depends in part upon our ability to expand our existing product families and to develop and market new products. The technology we currently use may be made obsolete by other competing or newly developed memory or other technologies. The development of new semiconductor designs and technologies typically requires substantial costs for research and development. Even if we are able to develop new products, the success of each new product depends on several factors including whether we selected the proper product and our ability to introduce it at the right time, whether the product is able to achieve acceptable production yields and whether the market accepts the new product. We cannot guarantee that we will be successful in developing new products or whether any products that we do develop will satisfy the above factors. In September 2003, we began shipping samples of our 1 megabit 3 volt nonvolatile semiconductor memory product. While we achieved production qualification on this product in September 2005, we cannot assure you that we will not discover technical problems or manufacturing concerns with this new product, that demand will continue to develop for the new product or that we will be able to continue to sell this new product at a profit.

THE CYCLICALITY OF THE SEMICONDUCTOR INDUSTRY MAY PREVENT US FROM MAINTAINING A CONSISTENT REVENUE STREAM AND MAY HARM OUR STOCK PRICE

The semiconductor industry has historically experienced significant peaks and valleys in sales volumes resulting in large variations of revenues and

resulting profits or losses. We do not have direct influence on the nature of the broad semiconductor market. Variations in the revenues and profits within the semiconductor industry may cause us to incur significant losses in the future. If the stock prices of many other semiconductor companies decrease, our stock price may also suffer.

IF WE FAIL TO SUCCESSFULLY IMPLEMENT PRODUCTS WITH CYPRESS SEMICONDUCTOR, OUR LIQUIDITY AND FUTURE REVENUES MAY SUFFER

On May 5, 2005, we closed a production and development agreement with Cypress Semiconductor Corporation to jointly develop an "S8" 0.13-micron SONOS nonvolatile memory production process. The production and development agreement also calls for Cypress to produce one or more Simtek products, as designated by Simtek, using the S8 process. We cannot assure you that we will be able to successfully develop and bring to qualified volume production products based on the S8 process or that Cypress will be able to develop embedded products contemplated to be developed using Simtek's intellectual property. If the development of the S8 process is delayed or fails, or if Cypress is unable to meet our production requirements, we might not be able to meet potential future orders planned to be received from our customers. This could significantly harm our revenue and future growth potential. We also entered into an escrow agreement pursuant to which we deposited \$3 million into an escrow account in order to support and make certain payments for the S8 process and product developments. If we fail to complete the development and production agreement, we might forfeit our rights to the escrow amount. This could harm our liquidity position.

OUR AGREEMENT WITH CYPRESS SEMICONDUCTOR CORPORATION MAY CONSUME OUR LIMITED RESOURCES OF ENGINEERS AND CONSUME A SIGNIFICANT AMOUNT OF OUR WORKING CAPITAL PREVENTING US FROM COMPLETING OTHER TASKS

Our production and development agreement with Cypress may consume a considerable amount of our engineering resources, which may limit the resources available to maintain or improve our production yields on our existing products and develop other new and derivative products. In addition to these indirect expenses related to our engineering resources, our obligations under the production and development agreement will consume a significant amount of our working capital until December 31, 2006. This may harm our business and stock price.

THE DECEMBER 30, 2005 SECURITIES PURCHASE AGREEMENT AND RELATED DOCUMENTS PROVIDE FOR CASH PENALTIES IF WE FAIL TO FOLLOW CERTAIN PROCEDURES OR MAINTAIN AN EFFECTIVE REGISTRATION RELATED TO THE SHARES PURCHASED BY SUCH INVESTORS

The Registration Rights Agreement entered into as part of the December 30, 2005 Securities Purchase Agreement amounting to \$11,000,000 contained a cash

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penalty provision if certain procedures are not followed or an effective Registration Statement is not maintained for the 68,750,000 shares purchased by investors. The cash penalties are 2% of the proceeds for each month that a breach occurs. We cannot assure you that we will be able to follow the required procedures or obtain or maintain such effective Registration Statement.

21

INTENSE COMPETITION IN THE SEMICONDUCTOR INDUSTRY MAY CAUSE US TO LOSE SALES REVENUE TO OTHER SUPPLIERS

There is intense competition in the semiconductor industry. We experience competition from a number of domestic and foreign companies, most of which have significantly greater financial, technical, manufacturing and marketing resources than we have. Our competitors include major corporations with worldwide silicon wafer fabrication facilities and circuit production facilities and diverse, established product lines. We also compete with companies, such as Ramtron International Corporation, attempting to obtain a share of the market for our product families. If any of our new products achieve market acceptance, other companies may sell competitive products at prices below ours. This would have an adverse effect on our operating results.

THE LOSS OF KEY EMPLOYEES COULD MATERIALLY AFFECT OUR FINANCIAL RESULTS

Our success depends in large part on our ability to attract and retain qualified technical and management personnel. There are limited personnel trained in the semiconductor industry resulting in intense competition for these personnel. If we lose any of our key personnel, this could have a material adverse affect on our ability to conduct our business and on our financial results.

OUR PATENTS MAY NOT PROVIDE US EFFECTIVE INTELLECTUAL PROPERTY PROTECTION; THIS COULD HARM OUR BUSINESS

We have been issued 17 U.S. patents (and assigned one other U.S. patent and three German patents) relating to specific aspects of our current products. We have also applied outside the United States for patents on our technology. We are not sure that any of the patents for which we have applied will be issued or, even if they are issued, will provide us with meaningful protection from competition. We may also not have the money required to maintain or enforce our patent rights. Notwithstanding our patents, other companies may obtain patents similar or relating to our patents.

We seek to protect a significant portion of our intellectual property as trade secrets, rather than patents. Unlike patents, trade secrets must remain confidential in order to retain protection as proprietary intellectual property. We cannot assure you that our trade secrets will remain confidential. If we lose trade secret protection, our business could suffer.

IF OUR PRODUCTS AND TECHNOLOGY INFRINGE ON THIRD PARTY PATENTS, OUR PRODUCT SALES OR GROSS MARGINS MAY SUFFER

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We have not determined whether our products are free from infringement of others' patents. If patent infringement claims are asserted against us and are upheld, we will try to modify our products so that they are non-infringing. If we are unable to do so, we will have to obtain a license to sell those products or stop selling the products for which the claims are asserted. We may not be able to obtain the required licenses. Any successful infringement claim against us, our failure to obtain any required license or requirement for us to stop selling any of our products, may force us to discontinue production and shipment of these products. This may result in reduced product sales and harm our revenues.

In 1998, we received notice of a claim for an unspecified amount from a foundation that owns approximately 180 patents and 70 pending applications. The foundation claimed that some of the machines and processes used in the building

22

of our semiconductor devices infringe on the foundation's patents. In April 1999, we reached an agreement with the foundation for us to purchase a nonexclusive license of the foundation's patents, based on our product offerings and sales forecast at that time. If our products or actual sales revenue vary significantly from the time of the agreement, we may be subject to additional payments.

In late 2002, we received notice of possible patent infringement from a corporation that has acquired a portfolio of patents. We have reviewed the claim and believe there are no potential infringements. We have received no further notification from this corporation. While there can be no assurances, if there are any infringements, we believe we will be able to enter into a licensing agreement with such company without any material impact on us.

FOREIGN CURRENCY EXCHANGE RATE FLUCTUATIONS MAY INCREASE OUR COSTS, LOWER OUR REVENUES AND CAUSE LOSS OF CUSTOMERS TO OUR COMPETITORS

We purchase materials, including silicon wafers, from outside the United States. Sales to customers located outside of the United States for the years ended December 31, 2005, 2004 and 2003 were 74%, 71% and 63%, respectively. We operate using United States dollars as the functional currency. Changes in foreign currency exchange rates can reduce our revenues and increase our costs. For example, our subcontractors may increase the prices they charge us, on a per purchase order basis, for silicon wafers if the United States dollar weakens. Any large exchange rate fluctuation could affect our ability to compete with manufacturers who operate using foreign currencies. We do not try to reduce our exposure to these exchange rate risks by using hedging transactions. Although we have not had any material losses due to exchange rate fluctuations over the last three years, we cannot assure you that we will not incur significant losses in the future.

BECAUSE OUR COMMON STOCK IS LISTED ONLY ON THE OTC ELECTRONIC BULLETIN BOARD, IT WILL BE MORE DIFFICULT TO SELL OUR COMMON STOCK

Our common stock is listed on the OTC Electronic Bulletin Board under the symbol "SMTE." Our common stock was listed on the NASDAQ Small-Cap Market until July 18, 1995, but, because we no longer met NASDAQ's listing requirements, our common stock transferred to the OTC Electronic Bulletin Board as mandated by NASDAQ rules. We may not be able to meet the requirements for relisting our common stock on NASDAQ or listing on any other exchange in the near future or in

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the longer term.

Securities that are not listed on the NASDAQ Small-Cap Market or other exchange are subject to a Securities and Exchange Commission rule that imposes special requirements on broker-dealers who sell those securities to persons other than their established customers and accredited investors. The broker-dealer must determine that the security is suitable for the purchaser and must obtain the purchaser's written consent prior to the sale. These requirements may make it more difficult for our security holders to sell their securities and may affect our ability to raise more capital. It may also make it harder for you to sell our stock than the stock of some other companies.

23

IF WE ISSUE SECURITIES AT LOW PRICES IN THE FUTURE, SOME OF OUR SECURITY HOLDERS MAY BE ENTITLED TO ACQUIRE MORE OF OUR SECURITIES, WHICH MAY DILUTE AND HARM THE HOLDERS OF OUR COMMON STOCK

We may be obligated under agreements with certain of our security holders to issue to them additional securities in exchange for little or no consideration if we sell our securities in the future at or below certain prices. The issuance of such securities could dilute and harm the holders of our common stock.

BECAUSE WE DO NOT INTEND TO PAY DIVIDENDS IN THE FORESEEABLE FUTURE, YOUR INVESTMENT RETURN MAY BE LIMITED

We have never paid cash dividends on our common stock. We do not expect to pay dividends in the foreseeable future. We intend to use any earnings to finance growth. You should not expect to receive dividends on your shares of common stock.

IF OUR BOARD OF DIRECTORS AUTHORIZES THE ISSUANCE OF PREFERRED STOCK, HOLDERS OF OUR COMMON STOCK COULD BE DILUTED AND HARMED

Our board of directors has the authority to issue up to 2,000,000 shares of preferred stock in one or more series and to establish the preferred stock's voting powers, preferences and other rights and qualifications without any further vote or action by the shareholders. The issuance of preferred stock by our board of directors could dilute and harm the rights of the holders of our common stock. It could potentially be used to discourage attempts by others to obtain control of us through merger, tender offer, proxy contest or otherwise by making such attempts more difficult to achieve or more costly. Given our present capital requirements, it is possible that we may need to raise capital through the sale of preferred stock in the future.

STANDARDS FOR COMPLIANCE WITH SECTION 404 OF THE SARBANES-OXLEY ACT OF 2002 ARE UNCERTAIN, AND IF WE FAIL TO COMPLY IN A TIMELY MANNER, OUR BUSINESS COULD BE HARMED AND OUR STOCK PRICE WOULD DECLINE.

Rules adopted by the Securities and Exchange Commission pursuant to Section 404 of the Sarbanes-Oxley Act require annual assessment of our internal control over financial reporting, and attestation of our assessment by our independent

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auditors. This requirement may apply to our Annual Report on Form 10-K for the fiscal year ending December 31, 2007, or based on certain qualifying 2006 standards, for the fiscal year ending December 31, 2006. The standards that must be met for management to assess the internal control over financial reporting as effective are new and complex, and require significant documentation, testing and possible remediation to meet the detailed standards. We may encounter problems or delays in completing activities necessary to make an assessment of our internal control over financial reporting. In addition, the attestation process by our independent auditors is new and we may encounter problems or delays in completing the implementation of any requested improvements or remediation and receiving an attestation of our assessment by our independent auditors. We can provide no assurance as to our, or our independent auditors', conclusions at December 31, 2006 (or 2007 as required by regulations), with respect to the effectiveness of our internal control over financial reporting. The above factors creates a risk that we, or our independent auditors, will not be able to conclude at December 31, 2006 (or 2007 as required by regulations)

24

that our internal controls over financial reporting are effective as required by the Sarbanes-Oxley Act. If we cannot assess our internal control over financial reporting as effective, or if our independent auditors are unable to provide an unqualified attestation report on such assessment, investors could lose confidence in our reported financial information and the trading price of our stock could drop.

Item 1B: Unresolved Staff Comments

None.

Item 2. Properties

We lease approximately 16,000 square feet of space in Colorado Springs, Colorado. This space includes a product engineering test floor of approximately 3,000 square feet. The lease is scheduled to expire on February 28, 2013. In February 2006, we entered into a lease agreement for our facility in Dresden, Germany, we lease approximately 2,800 square feet. The lease is schedule to expire on February 28, 2009.

We do not own any real property. We do not have a policy:

1. Limiting the percentage of assets which may be invested in any one investment or type of investment,
2. Regarding whether we acquire assets primarily for possible capital gain or primarily for income, or
3. With respect to investments in real estate, interests in real estate, real estate mortgages, or securities of or interests in persons primarily engaged in real estate activities.

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Item 3. Legal Proceedings

We are not a party to any legal proceeding (including where our property is the subject of the proceeding), and we are not aware of any proceeding that a government authority is contemplating as of the date of this report.

Item 4. Submission of Matters to a Vote of Security Holders

On October 27, 2005, we held an annual meeting of our shareholders for purposes of considering and voting upon a proposal to elect two directors to our board of directors each to serve for a three year term and considering and voting upon a proposal to ratify the selection of Hein & Associates LLP, independent auditors, as auditors of Simtek for the year ending December 31, 2005. As of the record date, September 12, 2005, we had 70,540,604 shares of common stock outstanding and the count of shares represented by proxy at the meeting was 62,165,595, a sufficient number of voting shares to constitute a quorum. Final results of the voting for each proposal is as follows:

25

1.	Election of Directors -----	For ---	Withheld -----
	Harold Blomquist	60,607,733	1,589,112
	Robert Pearson	60,837,477	1,351,368
2.	Ratification of Selection of Auditors -----		
	For ---	Against -----	Abstain -----
	61,011,707	366,492	818,646

26

PART II

Item 5. Market for Registrant's Common Equity, Related Stockholder Matters and

Issuer Purchases of Equity Securities

Our common stock is listed on the OTC Electronic Bulletin Board under the symbol "SMTE." Securities not included in the NASDAQ Small-CAP Market are covered by the Securities and Exchange Commission rule that imposes additional sales practice requirements on broker-dealers who sell such securities to persons other than established customers and accredited investors (generally institutions with assets in excess of \$5,000,000 or individuals with net worth in excess of \$1,000,000 or annual income exceeding \$200,000 or \$300,000 jointly with their spouse). For transactions covered by the rule, the broker-dealer must make a special suitability determination for the purchaser and receive the purchaser's written agreement to the transaction prior to the sale. Consequently, the rule may affect the ability of broker-dealers to sell our securities, which will have an adverse effect on the ability of our security holders to sell their securities and our ability to raise additional capital.

Shown below are the closing high bid and the closing low offer for our common stock as reported by the OTC Electronic Bulletin Board on the last day of the quarter.

Common Stock

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	High Bid -----	Low Bid -----
2004		
First Quarter.....	1.64	1.56
Second Quarter.....	.72	.68
Third Quarter.....	.62	.60
Fourth Quarter.....	.61	.59
2005		
First Quarter.....	.57	.56
Second Quarter.....	.375	.335
Third Quarter.....	.38	.34
Fourth Quarter.....	.29	.26

The quotations listed above reflect inter-dealer prices, without retail mark-up, mark-down or commission and may not represent actual transactions.

As of December 31, 2005, we had 469 shareholders of record. This number does not reflect shareholders who beneficially own common stock held in nominee or "street name".

We have not paid any dividends on our common stock since inception and we do not intend to pay any dividends on our common stock in the foreseeable future.

Harold Blomquist, our current President and Chief Executive Officer, purchased 200,000 shares of our common stock for \$108,400 on May 19, 2005, and

27

275,000 shares of our common stock for \$81,950 on November 9, 2005, in each case pursuant to Mr. Blomquist's employment agreement with us. We issued 150,000 shares of our common stock to Douglas Mitchell, our former President, Chief Executive Officer and Chief Financial Officer (acting), on June 15, 2005, and 50,000 shares of our common stock on November 25, 2005, in each case pursuant to the terms of his separation agreement with us. With respect to the issuances to Mr. Blomquist and Mr. Mitchell, we issued such securities in reliance upon Rule 506 promulgated under, and Section 4(2) of, the Securities Act, as each is or was an officer and director of Simtek, each is a sophisticated investor, each had access to material information of Simtek and there was no general solicitation.

On June 28, 2005, we issued warrants to purchase 200,000 shares of our common stock to the RENN Capital Group in exchange for a waiver of certain provisions relating to the 7.5% convertible debentures issued to the RENN Capital Group in 2002. These warrants have 5-year terms with an exercise price of \$0.50 per share. With respect to our June 28, 2005 transaction, we issued such securities in reliance upon Rule 506 promulgated under, and Section 4(2) of, the Securities Act, as the RENN Capital Group are sophisticated, accredited investors, there was no general solicitation and the RENN Capital Group had access to material information of Simtek.

In connection with an offering of \$11,000,000 of our common stock on December 30, 2005, we issued a warrant to purchase 1,062,500 shares of our

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common stock to C. E. Unterberg, Towbin, the investment banking firm that advised us in the December 30, 2005 offering, as partial payment for such services. This warrant has a five-year term and an exercise price of \$0.28 per share. With respect to our issuance to C. E. Unterberg, Towbin, we issued such securities in reliance upon Rule 506 promulgated under, and Section 4(2) of, the Securities Act, as such holder is sophisticated, an accredited investor, there was no general solicitation and such holder had access to material information of Simtek.

The following table sets forth information with respect to our equity compensation plans as of December 31, 2005.

Equity Compensation Plan Information

Plan Category	Number of securities to be issued upon exercise of outstanding options warrants and rights	Weighted-average exercise price of outstanding options warrants and rights	Number remaining future equity (exclud reflect
Equity compensation plans not approved by security holders	7,969,363	\$0.62	
Total	7,969,363	\$0.62	

Please see Note 6, "Stock Option Plans," to our Financial Statements included herewith.

28

Item 6: Selected Financial Data

The following selected financial data should be read in conjunction with, and are qualified in their entirety by, the consolidated financial statements and related notes thereto contained in "Item 8. Financial Statements and Supplementary Data" and "Item 7. Management's Discussion and Analysis of Financial Condition and Results of Operations" included herein.

	2005	Years Ended December 31,			2001
	-----	2004	2003	2002	-----
	(As Restated)	-----	-----	-----	-----
	-----	(in thousands, except per share data)			
Revenue	\$ 10,385	\$ 13,092	\$ 12,263	\$12,422	\$ 15,450
Gross margin	2,794	3,953	3,735	4,844	4,992
Loss from continuing					

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Operations	(7,490)	(3,731)	(2,389)	(1,028)	(925)
Income (loss) from					
Discontinued operation	1,704	60	116	65	(195)
Net loss	\$ (5,785)	\$ (3,670)	\$ (2,273)	\$ (963)	(1,120)
Loss per share from					
Continuing operations:					
Basic and diluted	\$ (.11)	\$ (.06)	\$ (.04)	\$ (.02)	\$ (.02)
Income per share from					
Discontinued operations:					
Basic and diluted	\$.03	\$.00	\$.00	\$.00	\$.00
Total loss per share					
Basic and diluted	\$ (.08)	\$ (.06)	\$ (.04)	\$ (.02)	\$ (.02)
Working capital	3,591	4,122	1,610	5,473	3,489
Total assets	18,758	7,976	7,303	7,932	6,587
Total long term debt	2,760	3,000	3,000	3,000	-
Shareholders' equity	11,319	1,989	2,523	3,253	4,230
Cash dividends per common					
Share (1)	-	-	-	-	-

(1) We have not declared any cash dividends on our common stock and do not expect to pay such dividends in the foreseeable future.

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Overview of Recent Debt and Equity Transactions

As described in "Item 5. Market for Registrant's Common Stock, Related Security Holder Matters and Issuer Purchases of Equity Securities", on May 5, 2005, we closed a share purchase agreement for a \$4,000,000 private placement of 6,740,816 shares of our common stock and warrants to acquire 5,055,612 shares of our common stock with Cypress Semiconductor Corporation, as well as a production and development agreement with Cypress to jointly develop an "S8" 0.13-micron SONOS nonvolatile memory production process. The production and development agreement also calls for Cypress to produce one or more Simtek products, as designated by Simtek, using the S8 process. The warrants have a 10-year term with an exercise price of \$0.7772.

On June 28, 2005, we issued warrants to purchase 200,000 shares of our common stock to the RENN Capital Group in exchange for a waiver of certain provisions relating to the 7.5% convertible debentures issued to the RENN Capital Group in 2002. These warrants have 5-year terms with an exercise price of \$0.50 per share.

On December 30, 2005, we issued to ZMD 6,260,713 shares of our common stock as partial payment for the assets we acquired from ZMD pursuant to the Asset Purchase Agreement, dated December 7, 2005, between us and ZMD.

On December 30, 2005, we issued, for an aggregate price of \$11,000,000, the amounts of shares indicated to the following investors: Crestview Capital Master LLC (24,687,500 shares); Straus Partners, LP (781,250 shares); Straus GEPT Partners, LP (781,250 shares); Big Bend XXVII Investments, L.P. (14,375,000 shares); Toibb Investment LLC (11,875,000 shares); Michael Seedman (625,000 shares); RENN Capital Group (9,375,000 shares); and SF Capital Partners Ltd. (6,250,000 shares). In addition, on December 30, 2005, we issued a warrant to purchase 1,062,500 shares of our common stock to C. E. Unterberg, Towbin, the investment banking firm that advised us in the December 30, 2005 offering, as partial payment for such services. This warrant has a five-year term and an exercise price of \$0.28 per share. We used the majority of the \$11,000,000 proceeds of the December 30, 2005 offering to fund the acquisition of assets from ZMD pursuant to the Asset Purchase Agreement, dated December 7, 2005, between us and ZMD.

In connection with the sale of \$11,000,000 of our common stock on December 30, 2005, instead of lowering the conversion price of the 7.5% convertible debentures issued to the RENN Capital Group in 2002, as required by the terms of the 2002 convertible debentures, from \$0.312 per share to \$0.16 per share as a result of the December 30, 2005 offering at \$0.16 per share, we agreed with the RENN Capital Group that the conversion price would only be lowered to \$0.22 per share as a result of the December 30, 2005 offering. As a result, instead of just 9,615,384 shares issuable upon conversion of the 2002 debentures (which would be the case were the conversion price still \$0.312 per share), there are

30

currently a total of 13,636,364 shares of common stock that are issuable to the RENN Capital Group upon conversion of the debentures as a result of the reduction of the conversion price to \$0.22 per share.

Also in connection with the sale of \$11,000,000 of our common stock on December 30, 2005, we agreed with Bluegrass Growth Fund LP, Bluegrass Growth Fund LTD and SF Capital Partners Ltd. that in exchange for their waiver of

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certain participation rights held by them in connection with the December 30, 2005 offering, the exercise price of their warrants to acquire 2,579,980 shares of our common stock would be lowered from \$0.627 per share to \$0.265 per share.

Results of Operations

General. We have designed and developed nvSRAM random access products since we commenced business operations in May 1987. We have concentrated on the design and development of our nvSRAM product families and technologies, marketing, distribution channels, and sources of supply, including production at subcontractors. During 2000, we added the capability to design, develop and produce gate array integrated circuits, or our logic products but ceased supporting this product as of December 31, 2003.

In 2003, we received notification from Chartered that they would be closing their silicon wafer fabrication facility #1 in March 2004 and that they would transfer our 0.8-micron process technology to their silicon wafer fabrication facility #2. Through late 2003 and into 2004, we began working with Chartered to transfer the production of our 16-kilobit, 64-kilobit, 5 volt 256-kilobit and 3 volt 256-kilobit product from their facility #1 to their facility #2. During the third and fourth quarters of 2004 and first quarter of 2005, we completed the transfer and qualification of these products. The transfer from Chartered's facility #1 to Chartered's facility #2 accounted for lower production yields through the first three quarters of 2004 as compared to the production yields we achieved in 2003. During the fourth quarter 2004, we began seeing production yields return to historic levels. Sales of our products manufactured from the silicon wafers we received from both of Chartered's facilities accounted for approximately 86%, 97% and 94% of our total revenue for the years ended December 31, 2005, December 31, 2004 and for December 31, 2003, respectively.

We have also been working with DongbuAnam on the development of a module which incorporates silicon-oxide-nitride-oxide- silicon technology, that will be used to manufacture both high density silicon oxide nitride oxide silicon flash and nvSRAM's for stand alone embedded products. The primary development product is our 1-megabit 3-volt nvSRAM. In September 2003, we began shipping samples of the 1-megabit 3-volt nvSRAM. In September 2005, we qualified our 1-megabit products for use in the commercial and industrial markets. During the third quarter of 2004, we began receiving initial production orders. Sales of our 1-megabit 3-volt products accounted for approximately 13% and 3% of our total revenue for the years ended December 31, 2005 and December 31, 2004, respectively.

As discussed previously, on December 30, 2005, we closed on the acquisition of certain assets related to ZMD's nvSRAM product line. This acquisition had no

31

effect on the operating results for fiscal year 2005, as there were no operating activities related to those assets, until January 2006.

Our programmed semiconductor logic products accounted for approximately 7% of our total revenue for the years ended December 31, 2003 and 0% of our revenue in 2004 or 2005. These products were discontinued in 2003.

Review of 2005 Operations

Total revenue for 2005 was approximately \$10,385,000. We saw a decrease in

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unit shipments and average selling prices of our commercial and industrial products. In 2005, management focused on realigning customer inventory and ordering patterns to more closely follow end user consumption patterns. This resulted in decreased revenue in 2005. Management believes that aligning customer unit consumption and ordering trends will ultimately allow both Simtek and its customers to better forecast production and inventory requirements. Revenue and unit volumes were lowest in the second quarter 2005 and showed steady improvement in the third and fourth quarters of 2005.

Review of 2004 Operations

Total product revenue for 2004 was approximately \$13,100,000. We saw an increase in unit shipments of our commercial products in 2004. The majority of this increase was for large production orders, with competitive bidding. Average selling prices were essentially the same in 2004 when compared to 2003. Revenues from our commercial nvSRAM products saw a total increase of approximately 8% in 2004 as compared to 2003. Revenues from our high-end industrial and military products saw an approximate increase of 58% in 2004 as compared to 2003. The increase was primarily due to completing shipments of our nonvolatile semiconductor memory products against on-going military contracts.

Results of Operations

Revenues

The following table sets forth our net revenues for semiconductor devices by product markets for the years ended December 31, 2005, 2004 and 2003 (in thousands):

	2005 ----	2004 ----	2003 ----
Commercial	\$ 8,669	\$ 10,314	\$ 9,548
High-end industrial and Military	\$ 1,617	\$ 2,778	\$ 1,759
Logic Products	\$ 99	\$ -	\$ 956
Total Revenue	\$ 10,385	\$ 13,092	\$ 12,263

Revenues for the year ended December 31, 2005 as compared to 2004

Commercial revenues include revenue generated from our legacy products and from our 0.25 micron products. Commercial revenues decreased by \$1,645,000 for the year ending December 31, 2005 as compared to the same period in 2004. The decrease was due: (i) lower average selling prices of our high volume legacy products due to competitive pricing; (ii) reduced unit volume with our key RAID customers due to competition; and, (iii) reduced unit volume due to a concerted effort to realign customer inventory and ordering patterns to more closely match actual consumption. The decrease in legacy unit shipments was partially offset

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by an increase in unit shipments of our 0.25 micron product family.

High-end industrial and military product revenues accounted for a decrease of \$1,161,000 for the year ending December 31, 2005 as compared to the same period in 2004. We saw an approximate 47% decrease in unit shipments and an approximate 42% decrease in average selling prices. The decreases in unit volume reflect our efforts to align customer inventory levels to their actual consumption of the products.

The \$99,000 increase in logic revenues was due to a last time buy of one of our discontinued logic products, which were discontinued in 2003.

Four distributors account for approximately 51% of our revenue for the year ended December 31, 2005 as compared to 49% for the same period in 2004. Products sold to distributors are sold without material recourse. Distributor contracts typically allow distributors to return up to 5% in value of product inventory in each six month period in exchange for a replacement order of equal value. This allows them to keep inventory current to market demand. Distributors sell our products to various end customers. If one of these distributors were to terminate its relationship with us, we believe that there would not be a material impact on our product sales, as other distributors would likely take their place.

We expect that revenue will increase in the next several quarters, as shipments of our 0.25 micron product family ramp up. Customer acceptance of our flagship 0.25 micron products has increased significantly in 2005. In addition, we expect that sales of our high-end industrial and military products will increase as customer inventory levels and consumption demand are realigned.

Revenues for the year ended December 31, 2004 as compared to 2003

Commercial product revenues increased by \$766,000 for the year ending December 31, 2004 as compared to the same period in 2003. The increase was due to an increase in unit volume of our commercial nonvolatile semiconductor memory products and the addition of our new 1-megabit nonvolatile semiconductor memory products.

High-end industrial and military product revenues accounted for an increase of \$1,019,000 for the year ending December 31, 2004 as compared with the same period in 2003. The increase was primarily due to completing shipments of our nonvolatile semiconductor memory products against on-going military contracts.

33

Revenues from our logic products decreased by \$956,000 for the year ending December 31, 2004 as compared to the same period in 2003. The decrease was due to our decision to eliminate this product line effective December 31, 2003.

Four distributors accounted for approximately 49% of our product revenue for the year ended December 31, 2004 as compared to 42% for the year ended December 31, 2003. Products sold to distributors are sold without significant recourse.

Cost of Sales and Gross Margins for the year ended December 31, 2005 as compared to 2004

We recorded cost of sales of \$7,591,000 and \$9,140,000 for the years ended December 31, 2005 and December 31, 2004, respectively. These costs reflect an

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approximate 3% decrease in gross margin percentage points for the year ended December 31, 2005 as compared to the same period in 2004. Actual gross margin percentages were 27% and 30% for the years ended December 31, 2005 and 2004, respectively. The decreases in gross margin percentages for the year ended December 31, 2005 were due primarily to decreased average selling prices and lower unit volume shipments of our high-end industrial and military products, which typically carry high gross margins.

During the year ended December 31, 2005, we purchased all of our silicon wafers to produce our 0.8 micron nvSRAM legacy products from a single supplier, Chartered, to support sales of our nonvolatile semiconductor memory legacy products. Sales of products built on these wafers accounted for approximately 86% of our revenue for the year ended December 31, 2005. We purchased silicon wafers to produce our family of 0.25 micron products, the 1-megabit and certain 256-kilobit nvSRAM products built on 0.25 micron technology from DongbuAnam. Sales of our semiconductor products built on 0.25 micron technology accounted for approximately 13% of our revenue for the year ended December 31, 2005. The remaining sales for the year were for a one-time sale of our discontinued logic products.

We expect gross margins on both our legacy and 0.25 micron products to improve during 2006. The net yields on the silicon wafers received from Chartered during the fourth quarter of 2005 and first two months of 2006 have resulted in yields significantly better and more stable than in earlier production received during the transition from Chartered's wafer fabrication facility # 1 to Chartered's wafer fabrication facility # 2. The yields are better than the historical yields from Chartered's wafer fabrication facility # 1. In addition, in 2006, we plan to move the final testing of our higher volume products to Asia, which will result in lower costs. On the .25-micron products, we achieved full production qualification on the 1-megabit products in September 2005. Based on that qualification, we have reduced the amount of back-end testing, which reduced total unit cost. We expect that we will continue to reduce the unit costs of the .25- micron product family in 2006, as yields improve and additional testing is eliminated.

34

Cost of Sales and Gross Margins for the year ended December 31, 2004 as compared to 2003

We recorded costs of sales of \$9,140,000 and \$8,528,000 for the years ended December 31, 2004 and December 31, 2003, respectively. These costs reflect an equivalent gross margin percentage for the year ended December 31, 2004 as compared to the year ended December 31, 2003. Actual gross margin percentages were 30% for the year ended December 31, 2004 and 2003.

Chartered closed its wafer fabrication facility #1 in March 2004 and we completed the transfer of the manufacturing of our silicon wafers into Chartered's facility #2 in the third and fourth quarter of 2004. The transfer from Chartered's facility #1 to Chartered's facility #2 accounted for lower production yields through the first three quarters of 2004 as compared to the production yields we achieved in 2003. During the fourth quarter 2004, we began seeing production yields return to historic levels.

Research and Development for the year ended December 31, 2005 as compared

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to 2004

We believe that continued investments in new product development are required for us to remain competitive in the markets we serve. In 2005, our research and development department continued its efforts on the final development, testing and qualification of our 1-megabit 3-volt nvSRAM with DongbuAnam. In September 2005, we qualified our 1-megabit products for use in the commercial and industrial markets. Development of the smaller 256-kilobit and 256-kilobit with real time clock built on the 0.25-micron base continued in 2005. We expect to achieve full production qualification for these products in the first half of 2006.

In addition, during the second half of 2005 we began development of our next generation nvSRAM product, in conjunction with Cypress Semiconductor, pursuant to the terms of the May 5, 2005 development agreement. This new product will be based on Cypress' .13-micron process and will include memory density of 4-megabits. We hope to provide customers with initial product samples in the fourth quarter of 2006.

Total research and development expenses were \$6,369,000 for the year ended December 31, 2005 as compared to \$4,942,000 for the year ended December 31, 2004.

The \$1,427,000 increase for the year was primarily due to a one-time charge of \$1,222,000 related to the final development of our 0.25-micron product. The one-time charge related to our 0.25 micron product was due to abnormally low yields and high scrap due to design and process issues with the silicon wafers. We have implemented a significant new revision for the silicon wafers being produced at DongbuAnam and preliminary testing shows a significant improvement in both the initial silicon wafer probe yield as well as the final assembly and test yield. The improved yields resulted in a more cost effective product. This charge was partially offset by decreases in payroll and payroll overhead costs of \$159,000, consulting services of \$312,000, product development costs of \$293,000 and equipment related costs of \$13,000 which were in turn partially offset by increases in qualification costs of \$60,000, and costs related to the joint development with Cypress of \$919,000. The \$159,000 decrease in payroll and payroll overhead costs was a direct result of reduced headcount. The \$312,000 decrease in consulting services was due to a decrease in engineering work performed by our wholly-owned subsidiary, Q-DOT, for the development of our data-communication products. As reported elsewhere in this Form 10-K, we sold substantially all the assets of Q-DOT in August 2005. The \$293,000 decrease in product development costs was related to wind down of development activities

35

related to the 0.25 micron product. As discussed above, the one-time charge related to our 0.25 micron product was due to abnormally low yields and high scrap due to design and process issues with the silicon wafers. These issues have been resolved and yields from the revised silicon wafers are significantly better and now result in a cost effective product. In September 2005, we achieved full production qualification of the 1-megabit product family.

Research and Development for the year ended December 31, 2004 as compared to 2003

Total research and development expenses were \$4,942,000 and \$3,987,000 for the years ended December 31, 2004 and December 31, 2003, respectively.

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The \$955,000 increase for the year was related to increases in payroll and payroll related costs of \$199,000, new product development costs of \$732,000, equipment leases, maintenance agreements for software and depreciation of \$20,000 and other expenses of \$4,000. The primary increase in payroll costs is related to an increase in employee headcount. Increased headcount and contract engineering services were required in order to develop our products in time to meet production schedules for the new products. The primary increase in product development costs was due to an increase in engineering materials and services such as, silicon wafer purchases, reticles, assembly and testing of our 1-megabit products from DongbuAnam and the development of our 64-kilobit and 256-kilobit products from X-FAB and 64-kilobit and 256-kilobit products from Chartered's wafer fabrication facility #2 and an increase in costs related to the commercial development of datacomm products performed by our Q-DOT subsidiary. The increase in product development costs included a one-time write off of capital purchases, of approximately \$61,000, related to the development at X-FAB that ended in August 2004. Equipment leases, maintenance agreements for software and depreciation are related primarily to software licenses and hardware required to design our new products.

Sales and Marketing for the year ended December 31, 2005 as compared to 2004

Total sales and marketing expenses were \$1,493,000 for the year ended December 31, 2005 as compared to \$1,608,000 for the year ended December 31, 2004.

The \$115,000 decrease was primarily due to decreases in advertising of \$15,000, sales commissions of \$285,000 and other miscellaneous expenses of \$11,000; these decreases were partially offset by an increase in payroll and overhead costs of \$196,000. The \$285,000 decrease in sales commission is a direct result of reduced revenue. The \$196,000 increase in payroll and overhead costs was the result of personnel changes.

Sales and Marketing for the year ended December 31, 2004 as compared to 2003

Total sales and marketing expenses were \$1,608,000 and \$1,213,000 for the year ended December 31, 2004 and December 31, 2003, respectively.

The \$395,000 increase for the year was related to an increase in payroll and payroll related costs of \$240,000, advertising of \$51,000, travel expenses of \$36,000, sales commissions of \$54,000 and other expenses of \$14,000. The increase in payroll and payroll related costs and travel was a direct result of increased headcount, the increase in advertising expenses were due to increased advertising for our new 1-megabit product. The increase in sales commissions is a direct result of increased revenue.

Administration for the year ended December 31, 2005 as compared to 2004

Total administration expenses were \$2,275,000 for the year ended December 31, 2005 as compared to \$917,000 for the year ended December 31, 2004.

The \$1,358,000 increase for the year was due to increases in accounting and

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legal expenses of \$123,000, \$343,000 increase in payroll and payroll overhead costs, \$713,000 in expenses related to separation and employment agreements and a \$179,000 increase in board of director costs and contract services. The increases in payroll and payroll related costs and contract services were related to increases in headcount and increases in administrative services provided by our subsidiary Q-DOT. The increases in accounting and legal expenses were related to increased activity related to agreements with personnel and increased securities work. The \$713,000 in expenses was related to costs associated with the terms of the employment agreement for our current Chief Executive Officer and the costs associated with the separation agreement entered into with our previous Chief Executive Officer.

Administration for the year ended December 31, 2004 as compared to 2003

Total administration expenses were \$917,000 and \$706,000 for the years ended December 31, 2004 and December 31, 2003, respectively.

The \$211,000 increase was due primarily to increases in accounting and legal fees of \$52,000, professional fees of \$47,000, costs associated with our annual meeting of shareholders of \$47,000, payroll and payroll related costs of \$35,000, and other miscellaneous expenses including travel of \$30,000. The increase in legal fees was primarily related to costs incurred in relation to our annual meeting of shareholders and increased legal fees related to our registration statements on Form SB-2 that we were required to file with the Securities and Exchange Commission. The increase in professional services was primarily due to an increase in fees paid to our Board of Directors and fees paid for financial consulting. The increase in accounting fees was due to increased audit fees related to our registration statements on Form SB-2. The increase in payroll and payroll overhead costs were primarily due to increased overhead costs.

Total Other Income (Expense) for the year ended December 31, 2005 as compared to 2004

The \$69,000 decrease in other income (expense) for the year ended December 31, 2005 as compared to the year ended December 31, 2004 was primarily due to an increase in interest income received from our restricted investments.

37

Total Other Income (Expense) for the year ended December 31, 2004 as compared to 2003

The \$2,000 decrease in total other income (expense) for the year ended December 31, 2004 as compared to the year ended December 31, 2003 was primarily related to a decrease in interest income which was a direct result of a decreased cash balance.

Loss from Continuing Operations for the year ended December 31, 2005 as compared to 2004

We recorded a loss from continuing operations of \$7,490,000 for the year ended December 31, 2005 as compared to a loss from continuing operations of \$3,731,000 for the year ended December 31, 2004. The increase of \$3,759,000 in net loss for the year was due primarily to an increase in operating expenses and decreased revenue discussed above.

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Loss from Continuing Operations for the year ended December 31, 2004 as compared to 2003

We recorded a loss of \$3,731,000 and \$2,389,000 for the years ended December 31, 2004 and December 31, 2003, respectively. The increase of \$1,342,000 in the loss from continuing operations for the year was due primarily to an increase in operating expenses discussed above.

Future Results of Operations

Our ability to be profitable will depend primarily on our ability to continue reducing manufacturing costs and increasing revenue by increasing the availability of existing products, by the introduction of new products and by expanding our customer base. We are also dependent on the overall state of the semiconductor industry and the demand for semiconductor products by equipment manufacturers and our ability to raise additional working capital.

On December 30, 2005, we purchased from ZMD certain assets related to ZMD's nvSRAM product line. We believe that this purchase will position us to be the number one supplier of nvSRAM products and will give us access to additional Tier 1 customers especially in Europe. Through this purchase we have regained sole ownership of our patents and technical information used by ZMD on SONOS-based standard product memory architectures previously licensed by us to ZMD and we have gained a license to other ZMD patents and technical information pertaining to SONOS-based nvSRAM. ZMD has entered into a 5-year non-compete agreement with us. In January 2006, we established a European design, customer service and support center in Dresden, Germany. We plan to use the new design team to accelerate product development and introduction of innovative new products designed for the 0.25 micron process at DongbuAnam and also targeted to the 0.13 micron process with Cypress.

In May of 2005, we entered into a Production and Development Agreement with Cypress to cooperate in developing a semiconductor process module that combines our nonvolatile technology with Cypress' advanced 0.13-micron digital complementary metal-oxide semiconductor, or CMOS, fabrication line. The module

38

incorporates SONOS technology, which is used to manufacture both high-density SONOS flash and SONOS nvSRAM's, for stand alone and embedded products.

As of December 31, 2005, we had a backlog of unshipped customer orders of approximately \$2,559,000, all of which we expect to ship by June 30, 2006. Orders are cancelable without penalty at the option of the purchaser prior to 30 days before scheduled shipment and therefore are not necessarily a measure of future product revenue.

We cannot assure you that the growth in demand, or demand for our products, will increase in the future. Through 2005, we were principally dependent on our legacy products for revenue, for which customer orders have declined over the past year. However, customer orders in the first quarter of 2006 have continued to increase consistent with the trend seen in the second half of 2005. We continue to explore alternatives to further reduce the cost to manufacture our existing products built on 0.8-micron and 0.25 micron technologies. We are currently reviewing additional cost reduction measures that may have the potential to improve our earnings.

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During the years ended December 31, 2005, 2004 and 2003, we purchased all of our silicon wafers to produce our legacy nvSRAM products from a single supplier, Chartered. Approximately 86%, 97% and 94% of our semiconductor device sales for 2005, 2004 and 2003, respectively, were from finished units produced from these silicon wafers. We believe that we maintain a very good relationship and that Chartered will continue to supply our wafer requirements for our legacy products. In addition, we may purchase additional 0.8-micron wafers from ZFoundry, a subsidiary of ZMD, as required to fill customer demand. DongbuAnam provides silicon wafers for our 0.25-micron products. Approximately 13% and 3% of our semiconductor product sales for the years ended December 31, 2005 and 2004 were from finished units produced from these silicon wafers.

We intend to continue designing, developing and subcontracting the production of our memory products. We also propose to continue to sell to existing and new customers through our normal sales and marketing channels.

Our ability to achieve profitability will depend primarily on our ability to continue reducing our manufacturing costs and increasing revenue by improving the availability of existing products, by the introduction of new products and by expanding our customer base. With the positive feedback we have received from the customers who have sampled and purchased products from our new 0.25 micron product family, we expect to continue ramping production of this product through 2006. In order to achieve these goals, we are dependent on the overall state of the semiconductor industry and the demand for semiconductor products by equipment manufacturers.

Liquidity and Capital Resources

As described in "Item 5. Market for Registrant's Common Stock, Related Security Holder Matters and Issuer Purchases of Equity Securities", on May 5, 2005, we closed a share purchase agreement for a \$4,000,000 private placement of 6,740,816 shares of our common stock and warrants to acquire 5,055,612 shares of

39

our common stock with Cypress Semiconductor Corporation, as well as a production and development agreement with Cypress to jointly develop an "S8" 0.13-micron SONOS nonvolatile memory production process. The production and development agreement also calls for Cypress to produce one or more Simtek products, as designated by Simtek, using the S8 process. The warrants have a 10-year term with an exercise price of \$0.7772.

On June 28, 2005, we issued warrants to purchase 200,000 shares of our common stock to the RENN Capital Group in exchange for a waiver of certain provisions relating to the 7.5% convertible debentures issued to the RENN Capital Group in 2002. These warrants have 5-year terms with an exercise price of \$0.50 per share.

On December 30, 2005, we issued to ZMD 6,260,713 shares of our common stock as partial payment for the assets we acquired from ZMD pursuant to the Asset Purchase Agreement, dated December 7, 2005, between us and ZMD.

On December 30, 2005, we issued, for an aggregate price of \$11,000,000, the amounts of shares indicated to the following investors: Crestview Capital Master LLC (24,687,500 shares); Straus Partners, LP (781,250 shares); Straus GEPT Partners, LP (781,250 shares); Big Bend XXVII Investments, L.P. (14,375,000

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shares); Toibb Investment LLC (11,875,000 shares); Michael Seedman (625,000 shares); RENN Capital Group (9,375,000 shares); and SF Capital Partners Ltd. (6,250,000 shares). In addition, on December 30, 2005, we issued a warrant to purchase 1,062,500 shares of our common stock to C. E. Unterberg, Towbin, the investment banking firm that advised us in the December 30, 2005 offering, as partial payment for such services. This warrant has a five-year term and an exercise price of \$0.28 per share. We used the majority of the \$11,000,000 proceeds of the December 30, 2005 offering to fund the acquisition of assets from ZMD pursuant to the Asset Purchase Agreement, dated December 7, 2005, between us and ZMD.

In connection with the sale of \$11,000,000 of our common stock on December 30, 2005, instead of lowering the conversion price of the 7.5% convertible debentures issued to the RENN Capital Group in 2002, as required by the terms of the 2002 convertible debentures, from \$0.312 per share to \$0.16 per share as a result of the December 30, 2005 offering at \$0.16 per share, we agreed with the RENN Capital Group that the conversion price would only be lowered to \$0.22 per share as a result of the December 30, 2005 offering. As a result, instead of just 9,615,384 shares issuable upon conversion of the 2002 debentures (which would be the case were the conversion price still \$0.312 per share), there are currently a total of 13,636,364 shares of common stock that are issuable to the RENN Capital Group upon conversion of the debentures as a result of the reduction of the conversion price to \$0.22 per share.

Also in connection with the sale of \$11,000,000 of our common stock on December 30, 2005, we agreed with Bluegrass Growth Fund LP, Bluegrass Growth Fund LTD and SF Capital Partners Ltd. that in exchange for their waiver of certain participation rights held by them in connection with the December 30, 2005 offering, the exercise price of their warrants to acquire 2,579,980 shares of our common stock would be lowered from \$0.627 per share to \$0.265 per share.

40

The change in cash flows for the year ended December 31, 2005 used in operating activities by continuing operations was primarily a result of a net loss of \$5,785,315, which was partially offset by \$433,181 in depreciation and amortization and a gain from discontinued operations of \$1,687,403. The changes in cash flow used in operating activities also reflected increases in allowance accounts, loss on disposal of assets, prepaid expenses and other, accounts payable and accrued expenses of \$22,650, \$129,307, \$622,004, \$767,512, and \$1,005,426 respectively. The increases were offset by decreases in accounts receivable of \$1,011,028, inventory of \$491,611 and customer deposits of \$47,464. The increase of \$622,004 in prepaid expenses and other was primarily due to a deposit put in place with a supplier. The \$129,307 loss on disposal of assets, was primarily related to the write off of test development software. The increase of \$1,005,426 in accrued expenses was primarily related to costs incurred with the December 30, 2005 stock transaction and expenses related to certain employment and separation agreements that had not been paid as of December 31, 2005. The \$491,611 decrease in inventory was primarily due to timing of inventory purchases. The change in cash flows provided by investing activities by continuing operations of \$1,526,233 was primarily due to the purchases of equipment required to test our nonvolatile semiconductor memory products and reticles required to produce our wafers, offset by the net proceeds of \$1,868,593 received from the sale of the assets of Q-DOT. The change in cash flows provided by financing activities by continuing operations of \$2,887,168 was primarily due to the equity financings, net of transaction related costs of \$3,944,403 and \$8,458,926 which we completed in May and December 2005,

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respectively. The proceeds of the equity financings were offset by the transfer of \$3,200,000 to escrow accounts for the Cypress and Q-DOT transactions and the cash portion of the purchase of certain assets from ZMD of \$7,685,416. Additional proceeds provided by financing activities included \$190,350 received from the sale of our common stock per employment agreements and \$310,501 received from the exercise of stock options by certain employees.

The change in cash flows for the year ended December 31, 2004 used in operating activities by continuing operations was primarily a result of a net loss of \$3,670,354, which was partially offset by \$442,245 in depreciation and amortization. The changes in cash flow used in operating activities also reflected increases in allowance accounts, loss on disposal of assets, accounts receivable, inventory, accounts payable, and accrued expenses of \$122,691, \$75,110, \$1,060,206, \$684,955, \$1,053,165, and \$81,972, respectively. The increase of \$1,060,206 in accounts receivable was directly related to the increase in revenue for the fourth quarter of 2004. The \$684,955 increase in inventory and \$1,053,165 increase in accounts payable was due to the receipt of raw materials at the end of December 2004 required to support first quarter 2005 shipments. The \$75,110 loss on disposal of assets, was primarily related to writing off the capital expenditures purchased for the installation of our process at X-FAB, terminated in August 2004. The change in cash flows used in investing activities by continuing operations of \$134,886 was primarily due to the purchase of equipment required to test our nonvolatile semiconductor memory products and reticles required to produce our wafers, offset by a \$300,000 release of restricted cash. The change in cash flows provided by financing activities by continuing operations of \$2,335,121 was primarily due to the equity financing of \$2,248,851 (net of transaction related costs) received in October 2004, payments on a line of credit of \$150,000, payments on capital leases of \$124,472 and \$360,742 received from the exercise of stock options by certain employees.

41

The change in cash flows for the year ended December 31, 2003 used in operating activities by continuing operations was primarily a result of a net loss of \$2,272,641, which was partially offset by \$469,538 in depreciation and amortization, decreases in allowance accounts, accounts receivable, inventory, and deferred revenue of \$16,376, \$496,530, \$411,358, and \$40,500, respectively. The decrease of \$496,530 in accounts receivable was related to timing of customer payments. The decrease in inventory was primarily due to an increase in finished goods shipments at the end of 2003. The change in cash flows used in investing activities by continuing operations of \$490,220 was primarily due to the purchase of equipment required to test our nonvolatile semiconductor memory products and software acquired for research and development activities. The cash flows provided by financing activities by continuing operations of \$1,640,296 was due to \$1,475,515 (net of transaction related costs) received from the November 2003 equity financing transaction, net borrowings on a line of credit of \$150,000, proceeds of \$183,131 for the exercise of stock options by certain employees partially offset by payments on a capital lease obligation of \$168,350.

Short-term liquidity.

Our cash balance at December 31, 2005 was \$1,765,774.

Our future liquidity will depend on our revenue growth, our ability to sell

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our products at positive gross margins and control of our operating expenses. Over the coming year, we expect to spend approximately \$11,500,000 for operating expenses assuming revenue growth and no significant change in marketing or product development strategies. We expect to meet these capital needs from sales revenues and, to the extent we do not have sufficient revenues, from our existing cash reserves. In addition, we are evaluating possible credit agreements, such as accounts receivable financing, although we have not reached any final agreement with any lender. If we are unable to meet our capital needs, through these means, it may be necessary for us to raise more capital.

Long-term liquidity.

We continue to evaluate our long-term liquidity. Our growth plans may require additional funding from outside sources. While we have no firm plans, we are in ongoing discussions with investment banking organizations and potential investors and lenders to ensure access to funds as required.

Critical Accounting Policies and Estimates

Simtek's consolidated financial statements have been prepared in accordance with accounting principles generally accepted in the United States of America, which require us to make estimates and judgments that affect the reported amounts of assets, liabilities, revenues and expenses and the related disclosures. A summary of these significant accounting policies can be found in Simtek's Notes to Consolidated Financial Statements included in this Form 10-K. The estimates used by management are based upon Simtek's historical experiences combined with managements understanding of current facts and circumstances. Certain of our accounting polices are considered critical as they are both important to the portrayal of our fi